

BEST AVAILABLE COPY

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. . NVIDP325 Applicant: Michael Johnson Filing Date: 09/27/2001	Application No.: 09/965,952 Group Art Unit: 2152
--	---	---

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
✓	B	TW 447205B	7/21/2001	TW	H04L	29/06		
✓	C	TW 448407B	8/1/2001	TW	G06F	9/45		
	D							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓	E	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series, <i>undated</i>
✓	F	Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993
✓	G	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
✓	H	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
✓	I	Muller, Raimund, "LON - das universelle Netzwerk" Elektronik 22/1991
✓	J	Rang, Michael; Tantawy, Ahmed, "A Design Methodology for Protocol Processors" IEEE 1995
Examiner <i>V. V</i>		Date Considered <i>1-31-05</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. NVIDP325	Application No.: 09/965,952
	Applicant: Michael Johnson	
	Filing Date: 09/27/2001	Group Art Unit: 2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
W	C	Banks, David and Prudence, Michael, "A High-Performance Network Architecture for a PA-RISC Workstation" IEEE Journal Vol. II, No. 22 February 1993
W	D	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications" ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997
W	E	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D. and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications" National Technical University of Athens, Greece, <i>undated</i>
W	F	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly with the PowerPC Architecture" IEEE 1997
W	G	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" IEEE March 1994
W	H	Wittie, Larry D., Ma, Fanyuan, "A TCP/IP Communication Subsystem in Micros" IEEE 1987
Examiner	V. W	Date Considered 1-31-05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified)	Atty. Docket No. NVIDP325	Application No.: 09/965,952
Information Disclosure Statement By Applicant	Applicant: Michael Johnson	
(Use Several Sheets if Necessary)	Filing Date: 09/27/2001	Group Art Unit: 2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
W	B	Orphanos, George; Birbas, Alexios; Petrellis, Nikos; Mountzouris, Ioannis; Malataras, Andreas, "Compensating for Moderate Effective Throughput at the Desktop" IEEE Communication Magazine April 2000
W	C	Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Alvin R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging" IEEE 1997
W	D	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM Systems" TranSwitch Corporation, <i>undated</i>
W	E	Bonjour, Dominique; de Hauteclouque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998
W	F	Kim, Chan; Jun, Jong-Jun; Park, Yeong-Ho; Lee, Kyu-Ho; Kim, Hyup-Jong, "Design and Implementation of a High-Speed ATM Host Interface Controller" Electronics and Telecommunications Research Institute, Korea, <i>undated</i>
W	G	Steenkiste, Peter, "Design, Implementation, and evaluation of a Single-copy Protocol Stack" Software - Practice and Experience, Vol. 28, June 1998
Examiner	V. W	
Date Considered	1-31-05	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (M dified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. NVIDP325	Application No.: 09/965,952
	Applicant: Michael Johnson	Group Art Unit: 2152
Filing Date: 09/27/2001		

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓	C	Traw, C. Brendan S.; Smith, Jonathan M., "Hardware/Software Organization of a High-Performance ATM Host Interface" IEEE 1993
✓	D	Nagata, Takahiko; Hosoda, Yamashita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Board" NTT Information and Communication Systems Laboratories 1998
✓	E	Nagata, Takahiko; Hosoda, Yasuhiro; Yamahsita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Boards: Driving Force for Various Multimedia Services" Vol. 9 No. 6 November 1997
✓✓	F	Jolitz, William Frederick, "High-Speed Networking: Header prediction and forward-error correction for very high-speed data transfer" Dr. Dobbs Journal, August 1992
✓✓	G	Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters" ISBN# 0-7803-2636-9, <i>undated</i>
✓✓	H	Wright, Maury "Intelligent Ethernet Boards" EDN June 23, 1988
Examiner <i>✓ ✓</i>		Date Considered <i>1-31-05</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. NVIDP325	Application No.: 09/965,952
	Applicant: Michael Johnson	Group Art Unit: 2152
Filing Date: 09/27/2001		

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
W	C	Marcus, William S.; Hadzic, Ilija; McAuley, Anthony J.; Smith, Jonathan M., "Protocol Boosters: Applying Programmability to Network Infrastructures" IEEE Communications Magazine October 1998
W	D	Korablum, Deborah F., "Protocol Implementation and Other Performance Issues for Local and Metropolitan Area Networks" IEEE 1988
VV	E	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC Approach to High Performance Network Interface Design: Protect4ed DMA and Other Techniques" IEEE 1997
VV	F	Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter" ACM SIGCOMM Computer Communication Review, <i>undated</i>
VV	G	Moldeklev, Kjersti; Klovning, Espen; Kure, Oivind, "The effect of end system hardware and software on TCP/IP throughput performance over a local ATM Network", <i>undated</i>
VV	H	Kanakia, Hermant; Cheriton, David R., "The VMP Network Adapter Board (NAB) High Performance Network Communication for Multiprocessors" ACM 1988
Examiner		Date Considered
J. VV		1-31-05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP325	09/965,952
	Applicant:	
	Michael Johnson	
	Filing Date:	Group Art Unit:
	09/27/2001	2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓✓	C	Chandrammenon, Grish P.; Varghese, George, "Trading Packet Headers for Packet Processing" IEEE 1996
✓✓	D	Nielson, Dr. Michael J.K., "TURBOchannel" IEEE 1991
✓✓	E	New Media News, www.newmedianews.com/02197/ts_inettuner.html , <i>undated</i> ✓
✓✓	F	Kelly, T. "Cheap Internet Hardware that Fits in Everything" ZDNet, www.zdnet.co.uk/news/1998/77/ns-5998.html
✓✓	G	Kitadeya et al. "Matsushita Launches WebTV Internet Connection Terminal" www.mei.co.jp/corp/news/official.data.dir/en981112-1/en981112-1.html , <i>undated</i> ✓
✓✓	H	eady Product Data Sheet, Internet Tuner, <i>undated</i> ✓
✓✓	I	Luijten, Ronald P., "An OC-12 ATM Switch Adapter Chipset" 1998 IEEE
✓✓		"Less-Numerical Algorithms", Ch. 20 p. 888-895, <i>undated</i> ✓
✓✓		"Storage Networking Industry Association" iSCSI Building Blocks for IP Storage Networking, www.ipstorage.org , <i>undated</i> ✓
Examiner	<i>v. Un</i>	Date Considered <i>1-31-05</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☒ **BLACK BORDERS**
- ☐ **IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- ☐ **FADED TEXT OR DRAWING**
- ☐ **BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- ☐ **SKEWED/SLANTED IMAGES**
- ☐ **COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- ☐ **GRAY SCALE DOCUMENTS**
- ☐ **LINES OR MARKS ON ORIGINAL DOCUMENT**
- ☐ **REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- ☐ **OTHER:** _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.